

Fig 2A

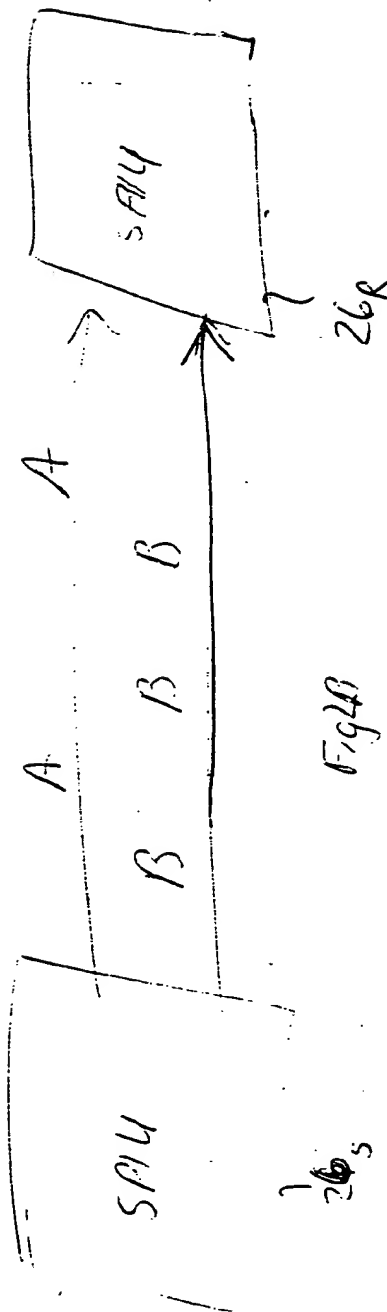


Fig 2B

320

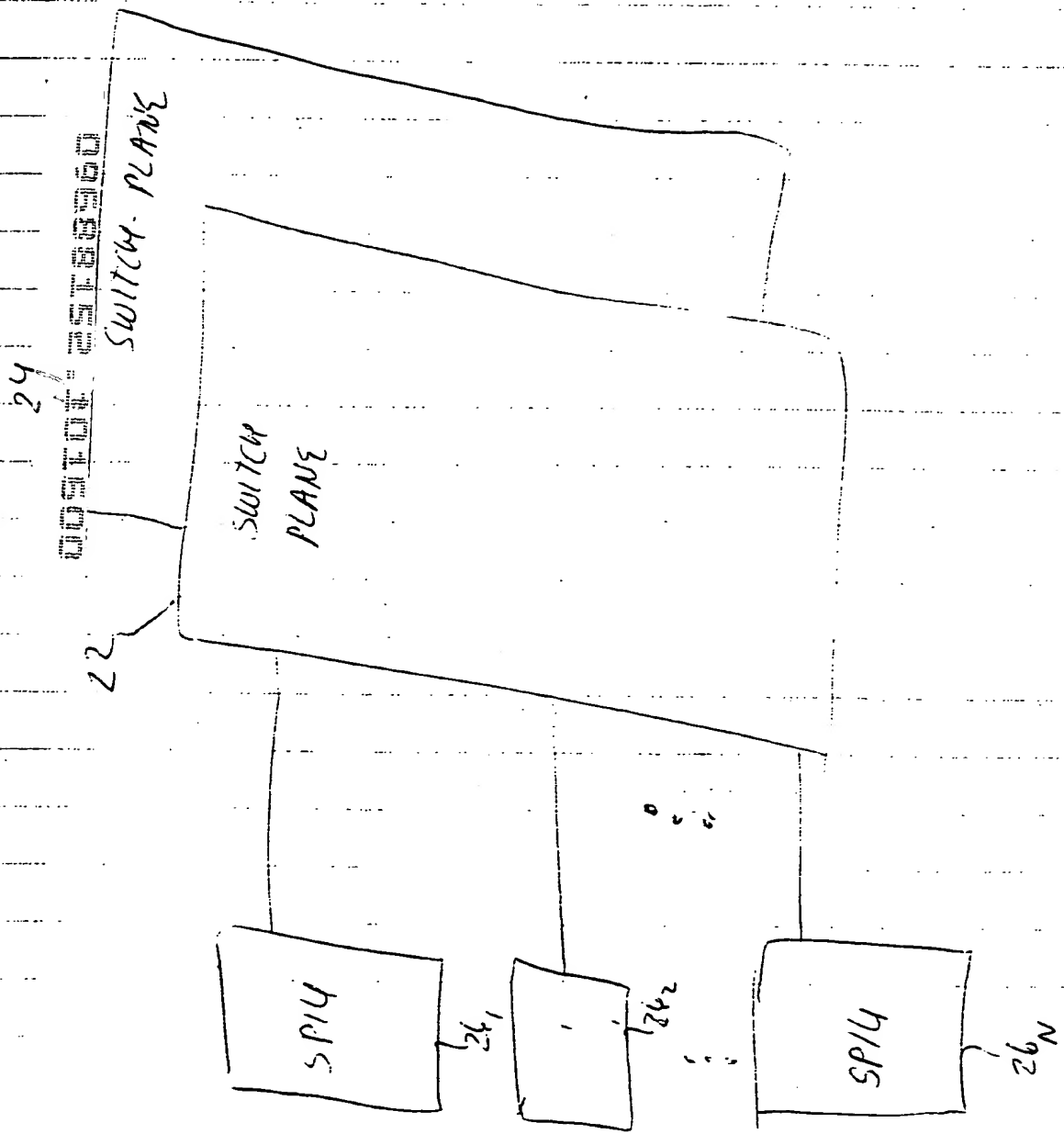


Fig 3

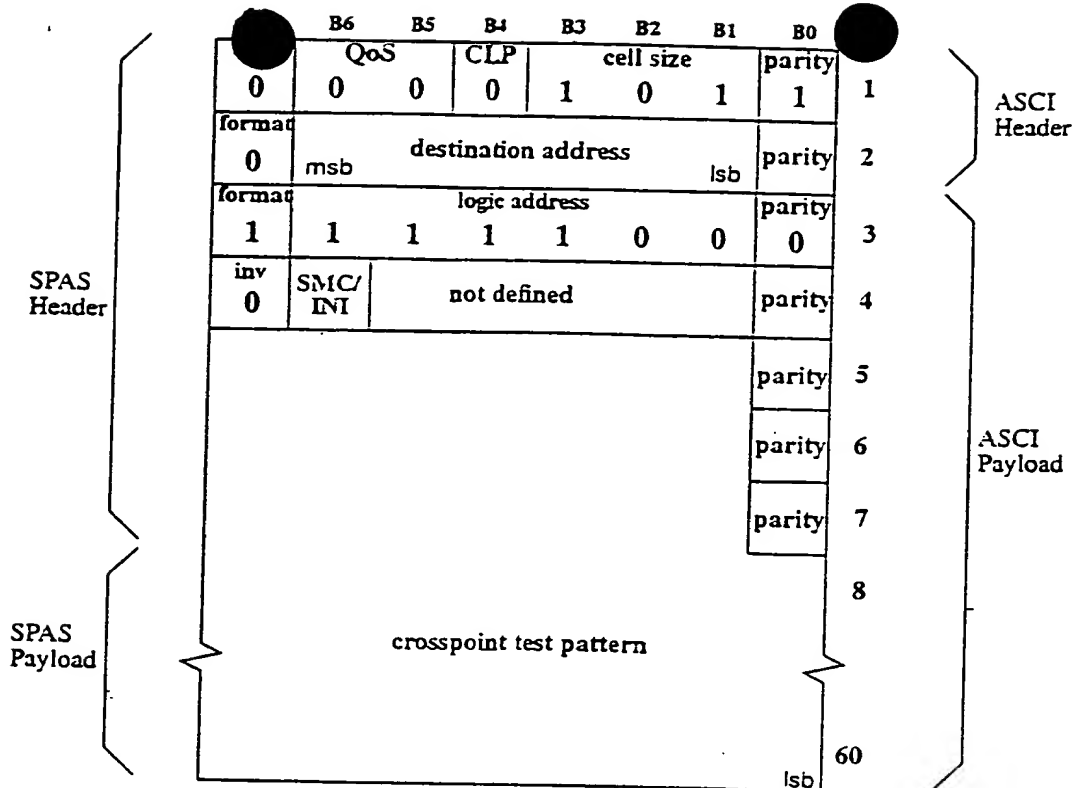


Fig 4B(1) Initialisation Cell

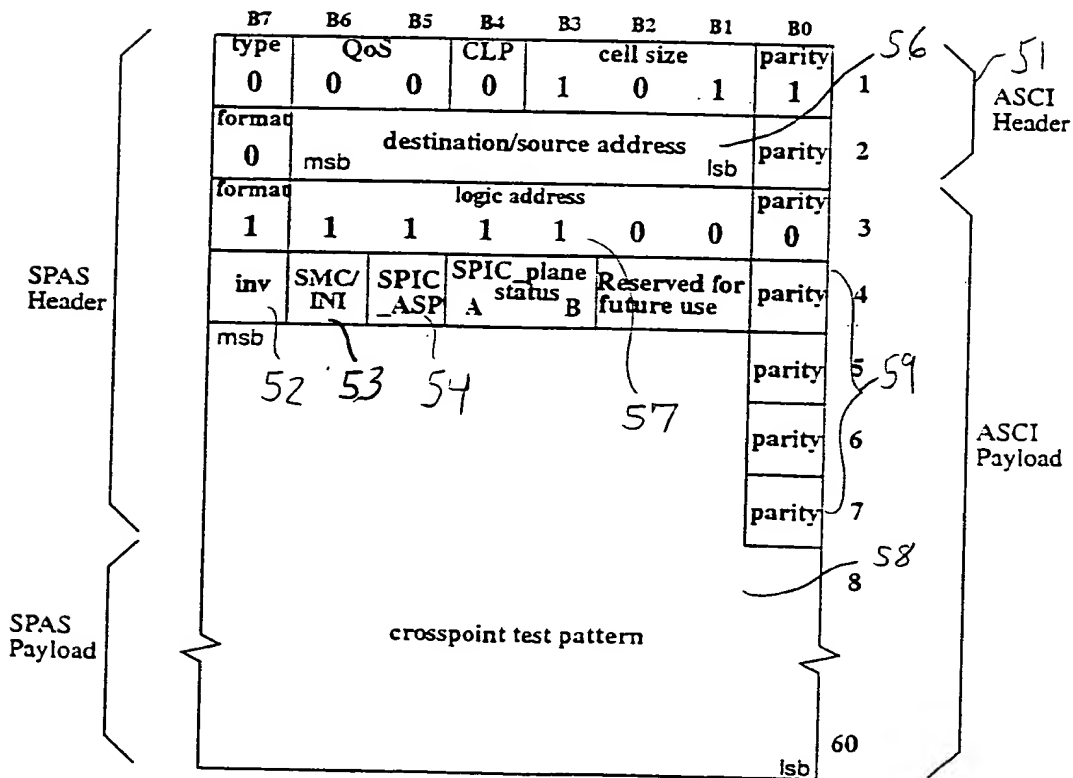


Fig 4B(2) Switch Maintenance Cell

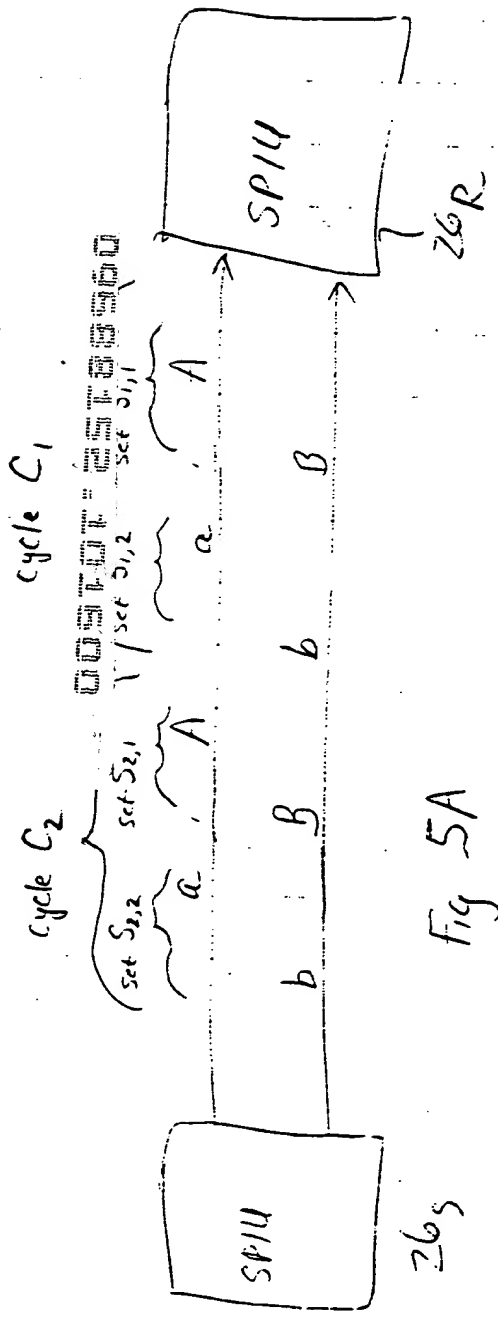


Fig 5A

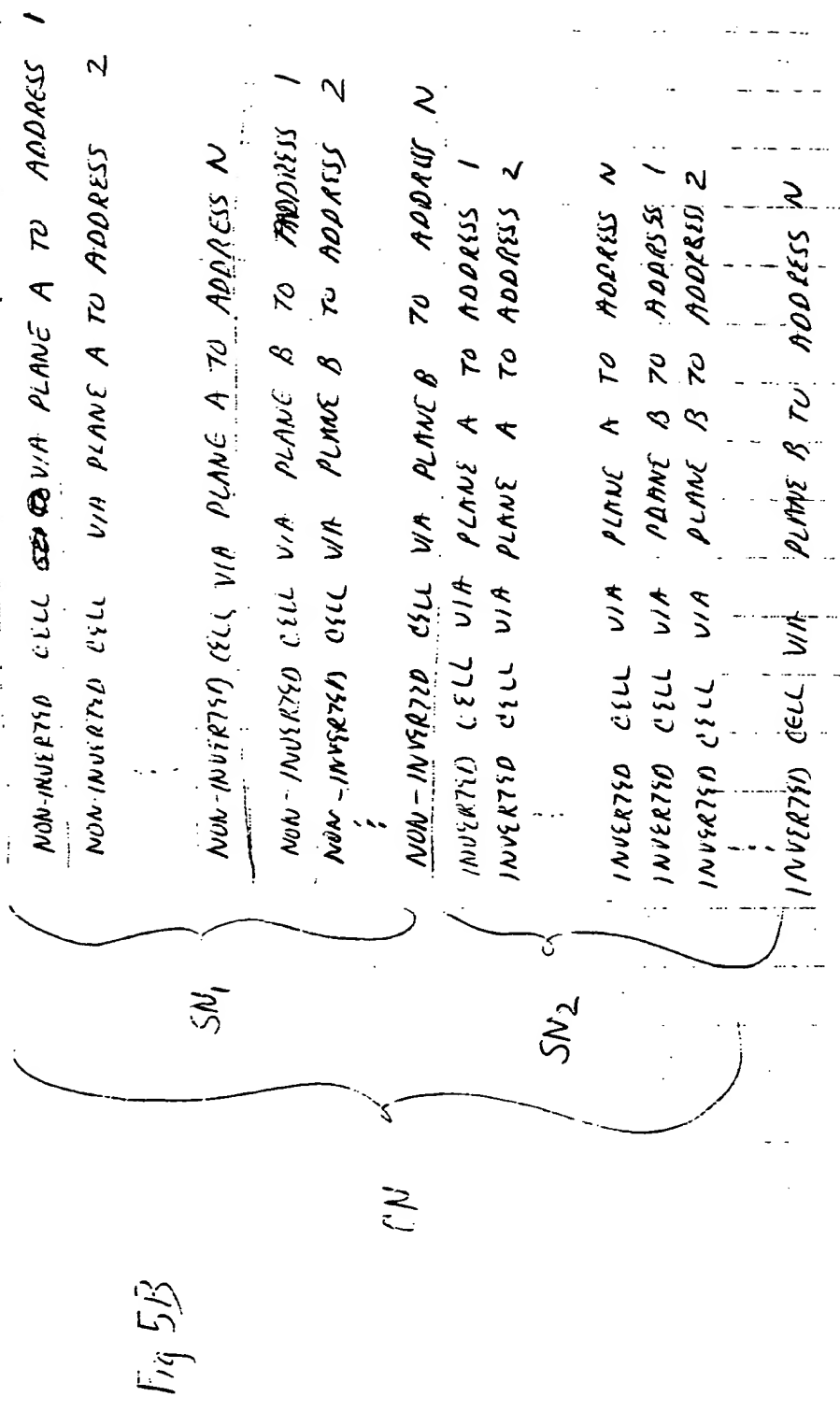


Fig 5B

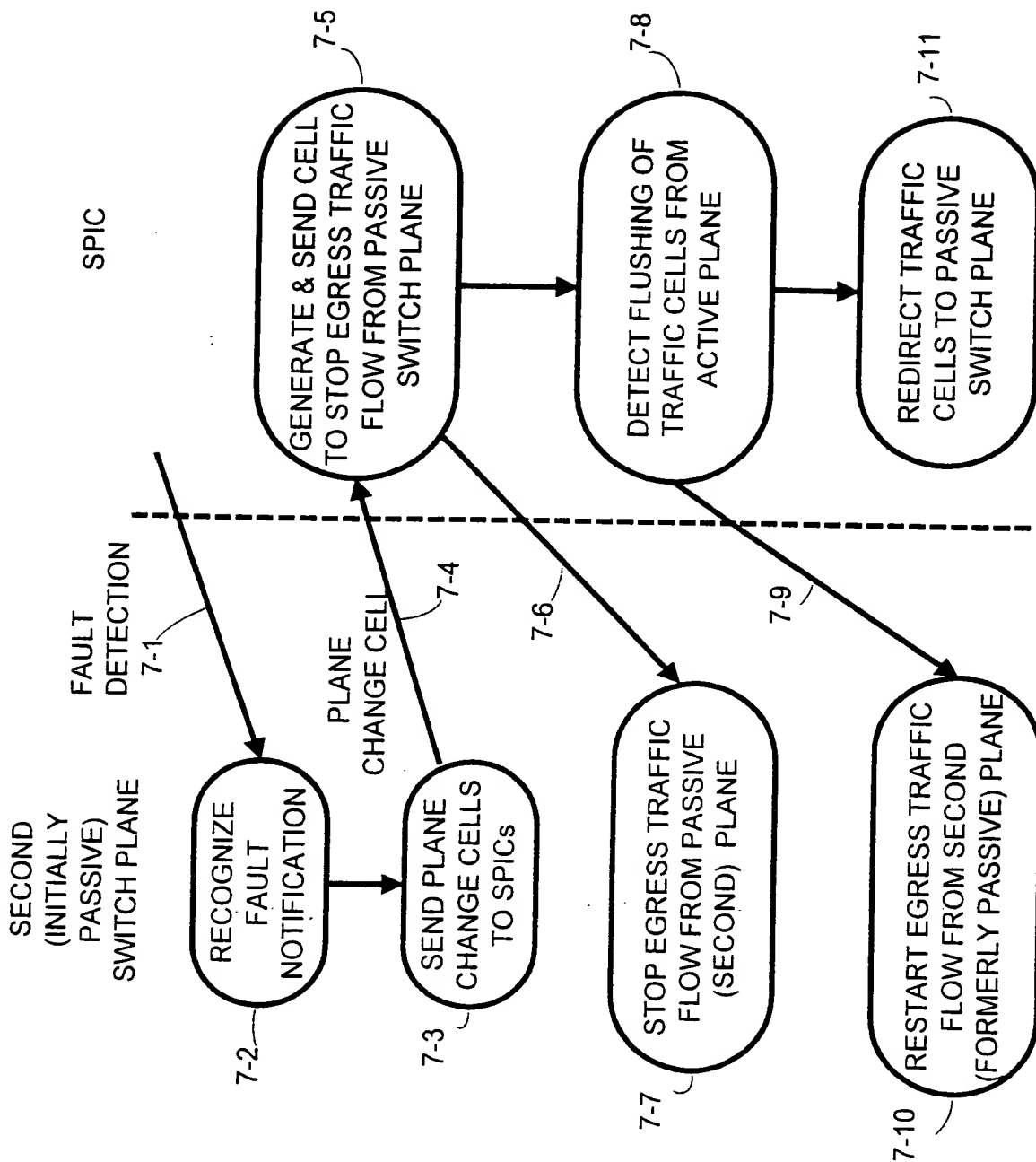


Fig. 7

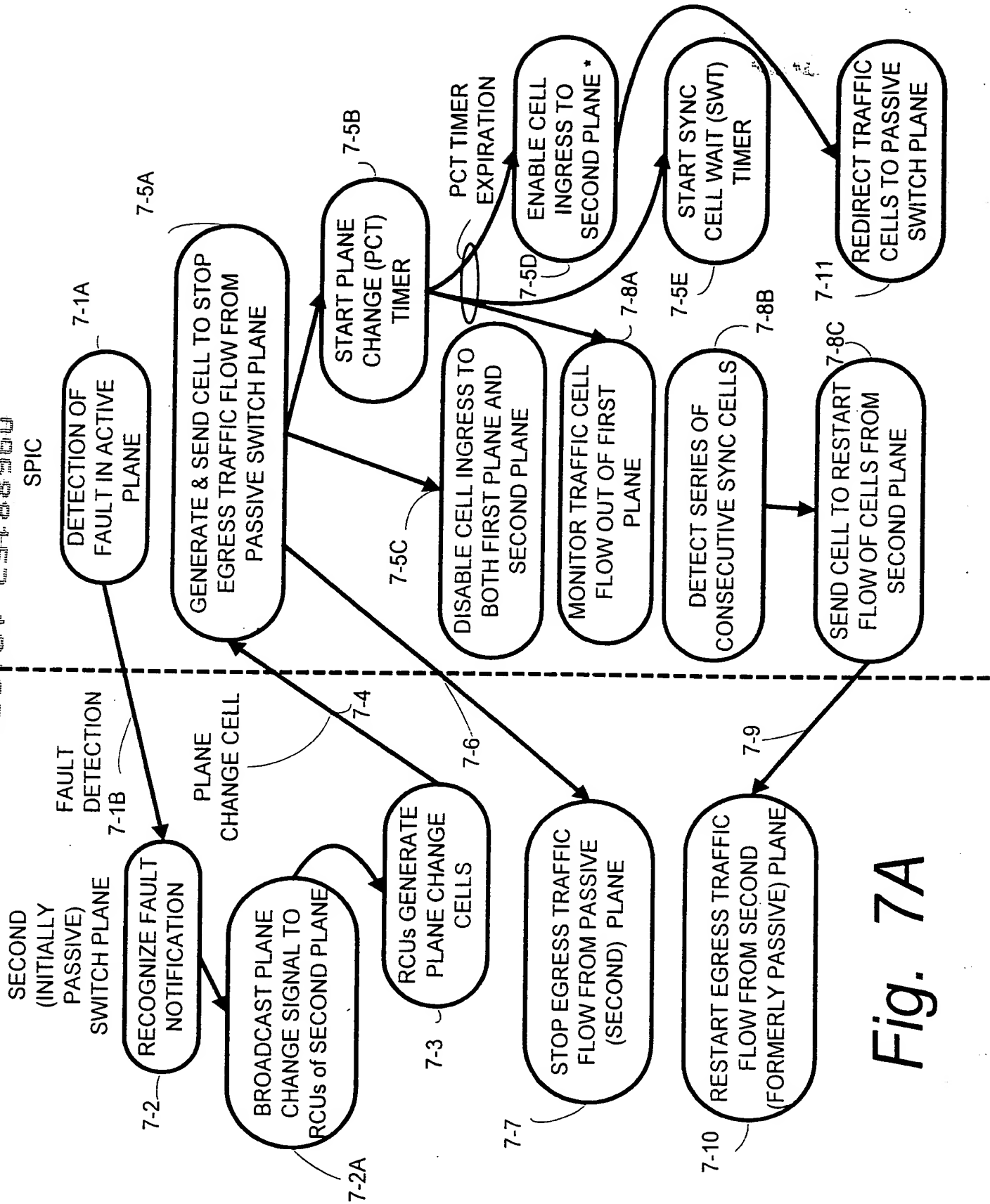


Fig. 7A

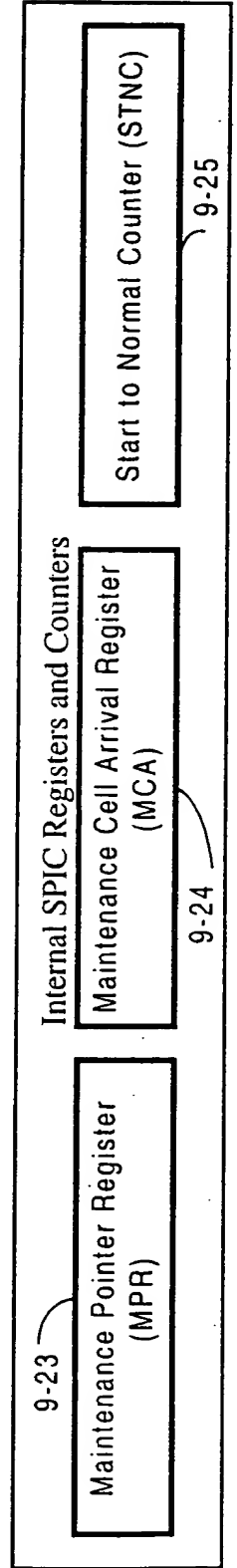
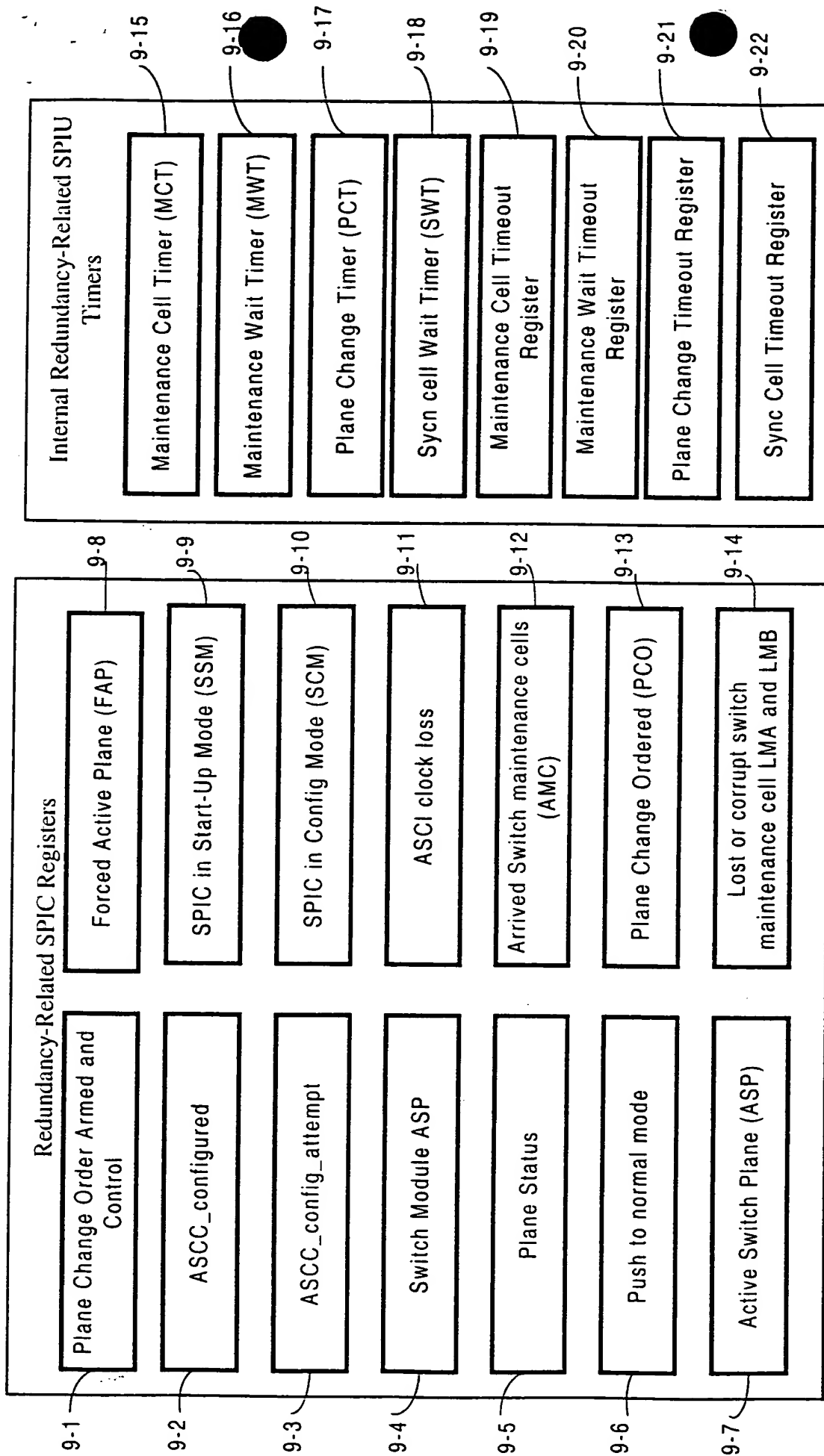


Fig. 9

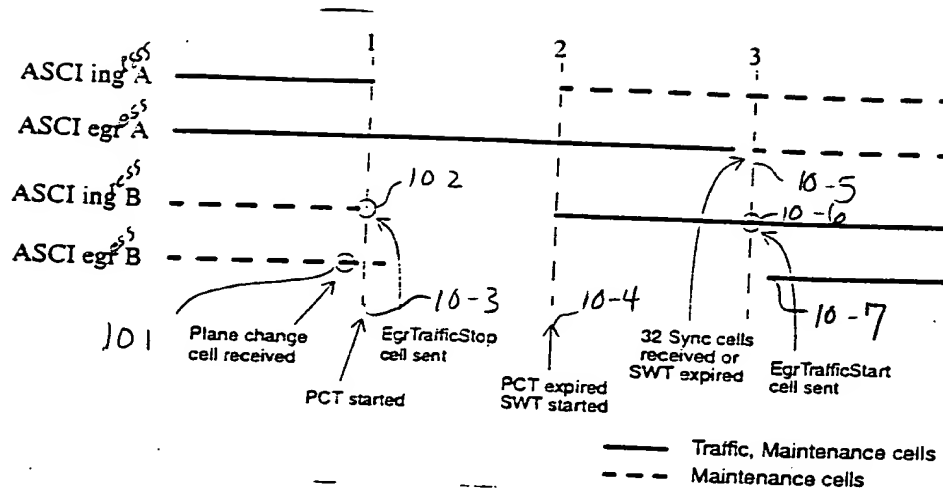
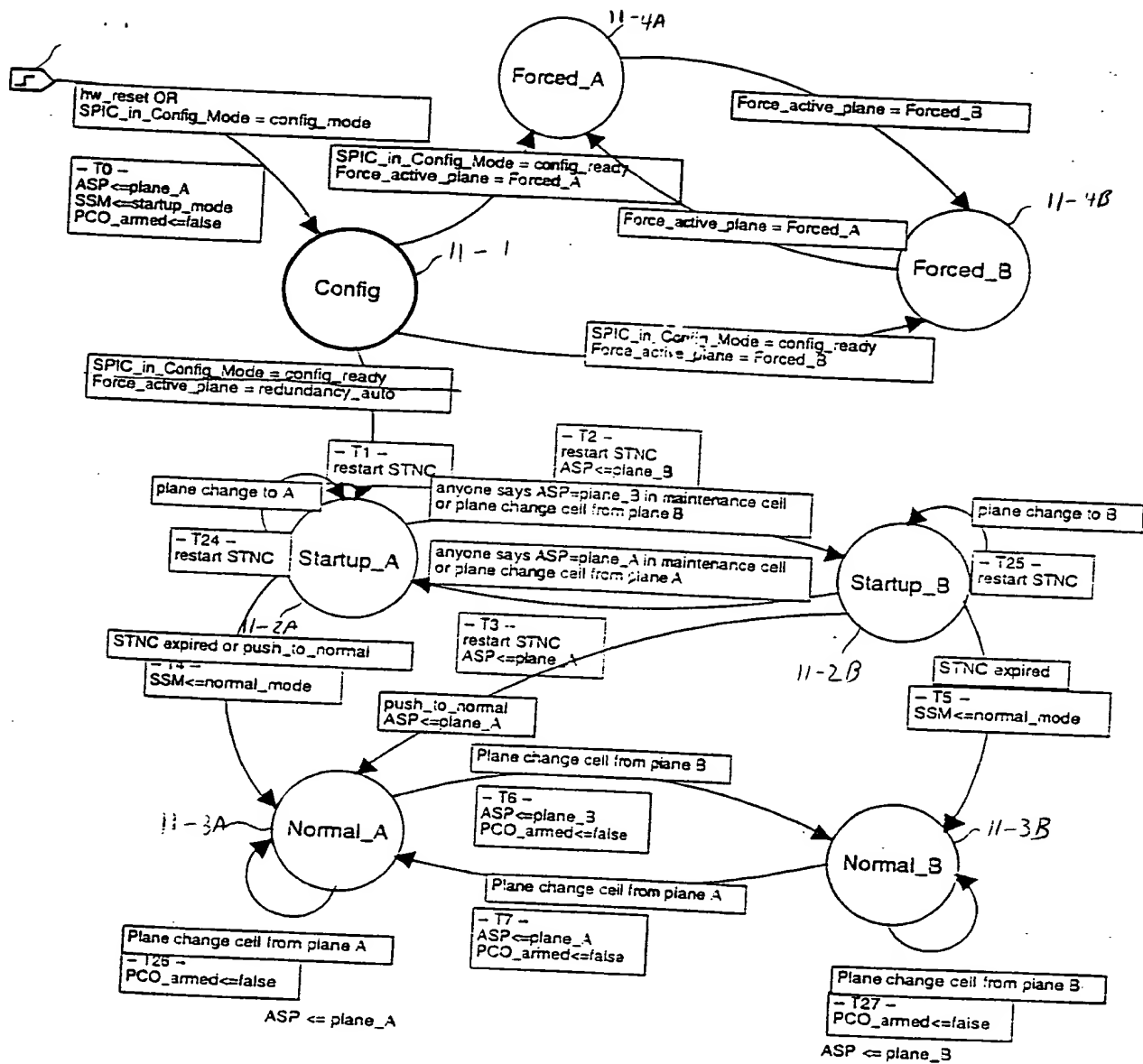


Fig 10

005101-25183960



ASP = Active switch plane
 SSM = SPIC in start-up mode
 PCO_armed = Plain change order armed
 STNC = Start to normal counter

Fig 11

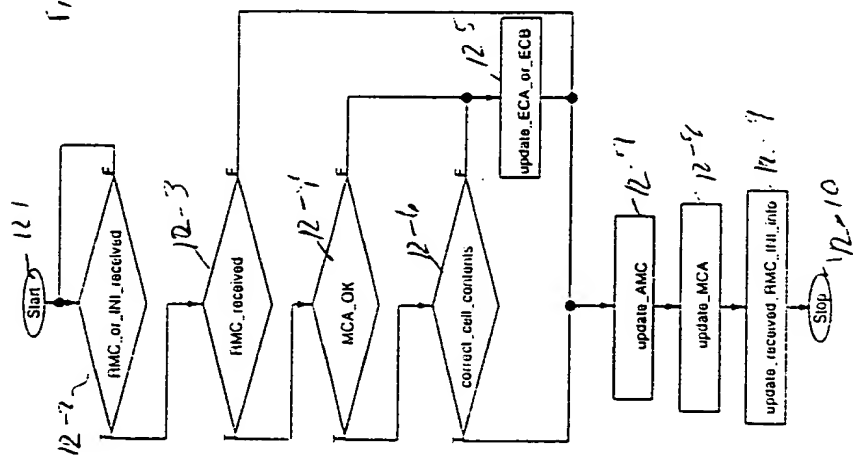


Fig 12

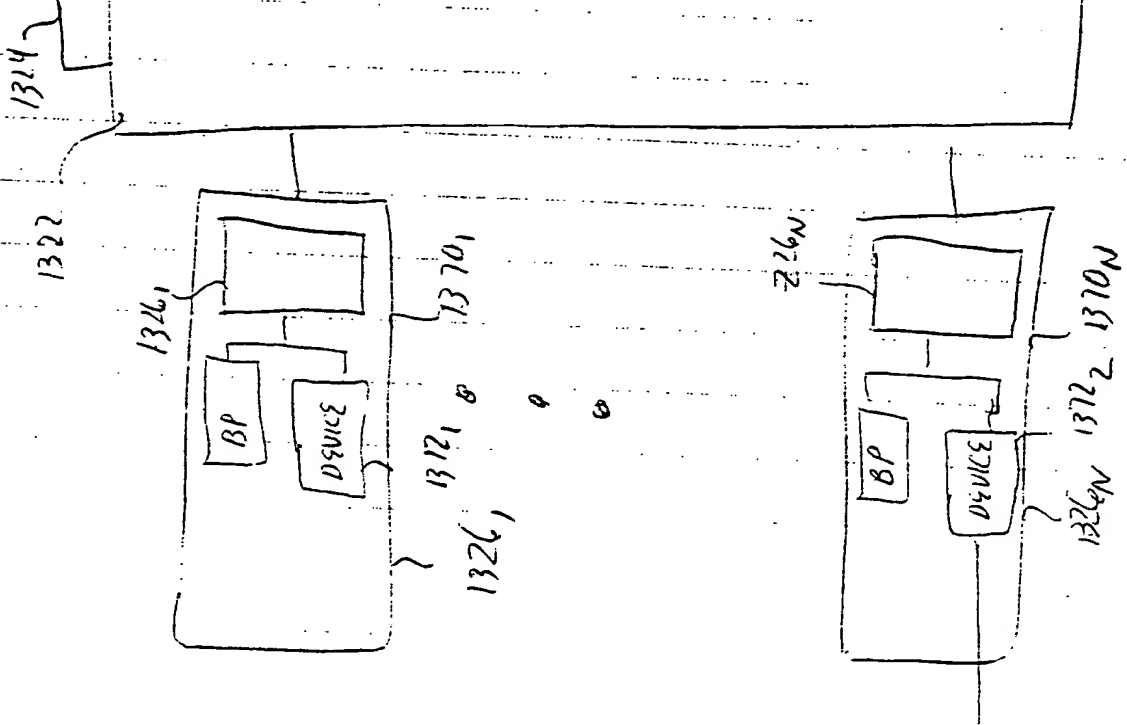


Fig 13